

AMENDMENTS TO THE SPECIFICATION

Please replace Paragraph **[0024]** with the following paragraph:

[0024] FIG. 11 is a block diagram of a VLO despreaders; **[[and]]**

Please add the following paragraphs:

[0025.1] FIG. 13 is an exemplary illustration of binary processing of a transmitted information bit stream;

[0025.2] FIG. 14 is an exemplary illustration of binary processing of a received information bit stream; and

[0025.3] FIG. 15 is an exemplary illustration of binary processing for transmitting and receiving a bit stream, in accordance with various implementations of the present invention.